



PATENT *fw*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:
DEB et al.

Application No.: 10/753,846

Filed: January 7, 2004

For: METHOD AND APPARATUS FOR
SEMANTIC PROCESSING ENGINE

)
Group Art Unit: 2644
)
Examiner: Unassigned
)
Atty. Docket No.: XAMBP001A
)
Date: August 24, 2006
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on August 24, 2006.

Signed: 
Sylvia Castillo

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, (the copies of references which are not U.S. Patents have been attached), may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to consider these references and to acknowledge such consideration by initialing the appropriate location on the attached PTO Form 1449.

This Information Disclosure Statement (IDS) is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This IDS is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this IDS. However, if it is determined that any fees are due, then the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. XAMBP001A)

Respectfully submitted,
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Sheet

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of

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Complete if Known

Application Number	10/753,846
Filing Date	January 7, 2004
First Named Inventor	DEB et al.
Art Unit	2644
Examiner Name	Unassigned
Attorney Docket Number	XAMBP001A

U.S. Patent Documents

Examiner Initials	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/JR/	A	2006/0149840	07/06/06	THOMPSON et al.	
	B				
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Foreign Patent Documents

Examiner Initial	Cite No. ¹	Foreign Patent Document No.	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear	T ⁶
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Non Patent Literature Documents

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/JR/	G	OBERG, Johnny, "ProGram: A Grammar-Based Method for Specification and Hardware Synthesis of Communication Protocols," Royal Institute of Technology, Department of Electronics, Electronic System Design, 1999, Stockholm, Sweden
/JR/	H	AnaGram, "Introduction to Syntax Directed Parsing," Copyright 1993-2002, Parsifal Software, http://www.parsifalsoft.com/isdp.html , Pages 1-10
/JR/	I	Hiran Ramankutty, "Yacc/Bison - Parsers Generators - Part 1," Copyright 2003, Published in Issue 87 of Linux Gazette, February 2003, http://www.linuzgazette.com/copying.html , Pages 1-7
/JR/	J	M.E. Lesk et al., "Lex - A Lexical Analyzer Generator," 1975 as per Citeseer.
/JR/	K	Zerksis D. Umrigar, "Introduction to Shift-Reduce Parsing," Copyright 1997, http://www.cs.binghamton.edu/~zdu/parsdemo/sintro.html , Pages 1-6
/JR/	L	"Lexical analysis," Wikipedia, the free encyclopedia, 1/2006 http://en.wikipedia.org/wiki/Lexer , Pages 1-2

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Substitute for form 1449/PTO				Complete if Known	
				Application Number	10/753,846
				Filing Date	January 7, 2004
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				Art Unit	2644
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Sheet	2	of	2	Attorney Docket Number	XAMBP001A

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/JR/	G	DONNELLY et al., "Bison, The YACC-compatible Parser Generator, November 1995, Bison Version 1.25
/JR/	H	"LR parser," Wikipedia, the free encyclopedia, 1/2006 http://en.wikipedia.org/wiki/LR_parser , Pages 1-7
/JR/	I	Vern Paxson, "Flex, version 2.5" A fast scanner generator, Edition 2.5, March 1995
/JR/	J	Biswadeep Nag, Staff Engineer, Performance Engineering, Sun Microsystems, "Acceleration Techniques for XML Processors," XML Conference, 2004
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